

## REMARKS

Applicant requests favorable reconsideration and allowance of this application in view of the foregoing amendments and the following remarks.

Claims 1-30, 32 and 33 are pending in this application, with Claims 1, 16, and 30 being independent. Claim 31 has been cancelled without prejudice.

Claims 1-3, 5, 16-18, 20, 30, 32, and 35 have been amended. Applicant submits that support for the amendments can be found in the original disclosure at least, for example, in Fig. 4 and the corresponding description in the specification. Therefore, no new matter has been added.

Claims 1-8, 10, 13-21 and 29-33 have been rejected under 35.U.S.C. §102(e) as being anticipated by U.S. Patent No. 6,470,380 (Yoshizawa et al.). Claims 9, 11-12, and 22-28 have been rejected under 35.U.S.C. §103(a) as being unpatentable over U.S. Patent No. 6,470,380 (Yoshizawa et al.) and in further view of U.S. Patent No. 6,088,454 (Nagashima). Applicant respectfully traverses these rejections in view of the foregoing amendments and the following remarks.

As recited in independent Claim 1, the present invention includes, *inter alia*, the features of receiving a first physical address designating a storage position for holding data in a storage means and converting the first physical address into a second physical address of the storage means based on an inputted key code. Applicant submits that the cited art fails to disclose or suggest at least this feature.

Yoshizawa et al. discloses that a memory is accessible from an external bus only when data matching key information is provided from the external bus. See col. 3, lines 20-25. Also, that patent discloses that a host-processor virtual-address space 51 is

converted into a host-processor physical address space 52, and that an accelerator physical-address space 53 is allocated to the host-processor physical-address space 52 and an accelerator processor-address space 54. However, Applicant submits that Yoshizawa et al. does not disclose or suggest converting a first physical address to a second physical address based on a key code.

The other cited art also fails to disclose or suggest at least the above-mentioned features, and therefore fails to remedy the deficiencies of Yashizawa et al.

Accordingly, Applicant submits that the present invention recited in independent Claim 1 is patentable over the cited art, whether that art is taken individually or considered in combination. Independent Claims 16 and 30 recite a similar feature and are believed patentable for similar reasons. The dependent claims are believed patentable for at least the same reasons as the independent claims they depend from, as well as for the additional features they recite.

Applicant's undersigned attorney may be reached in our Washington, D.C. office by telephone at (202) 530-1010. All correspondence should continue to be directed to our below-listed address.

Respectfully submitted,



Attorney for Applicant

Brian L. Klock

Registration No. 36,570

FITZPATRICK, CELLA, HARPER & SCINTO  
30 Rockefeller Plaza  
New York, New York 10112-3801  
Facsimile: (212) 218-2200  
BLK/lmj